An Improved IUPQC with Fuzzy Controller Providing Grid Voltage Regulation as a STATCOM

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Abstract: This paper presents using fuzzy logic an improved controller for the dual topology of the Unified Power Quality Conditioner (iUPQC) extending its applicability in power-quality compensation, as well as in microgrid applications. Beyond the conventional UPQC power quality features, including voltage sag/swell compensation, the iUPQC will also provide reactive power support to regulate not only the load-bus voltage but also the voltage at the grid-side bus by using this controller. The iUPQC will work as a Static Synchronous Compensator (STATCOM) at the grid side, while providing also the conventional UPQC compensations at the load or microgrid side by using fuzzy. simulation results are provided to verify the new functionality of the equipment.

Keywords: UPQC, microgrids, power quality, Static Synchronous Compensator (STATCOM), Unified Power Quality Conditioner (UPQC), fuzzy logic controller.

1.0 INTRODUCTION

In contrast. power-electronics-driven loads generally require ideal sinusoidal supply voltage in order to function properly, whereas they are the most responsible ones for abnormal harmonic currents level in the distribution system. In this scenario, devices that can mitigate these drawbacks have been developed over the years. Certainly, power-electronics devices have brought about great technological improvements. However, the increasing number of power-electronics-driven loads used generally in the industry has brought about uncommon power quality problems. Some of the solutions involve a flexible compensator, known as the Unified Power Quality Conditioner (UPQC) [1]–[7] and the Static Synchronous Compensator (STATCOM) [8]-[13].

Power circuit of a UPQC consists of the combination of a shunt active filter and a

series active filter connected in a back-to-back configuration. This combination allows the simultaneous compensation of the load current and the supply voltage, so that the compensated current drawn from the grid and the compensated supply voltage delivered to the load are kept balanced and sinusoidal. The fuzzy logic and dual topology of the UPQC, i.e., the iUPQC, was presented in [14]–[19], where the shunt active filter behaves as an ac-voltage source and the series one as an ac-current source, both at the fundamental frequency. This is a key point to better design the control gains, as well as to optimize the LCL filter of the power converters, which allows improving significantly the overall performance of the compensator [20].

Dynamic reactive power compensation that means of the STATCOM has been used widely in transmission networks to regulate the voltage. Nowadays, the STATCOM is largely used for

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voltage regulation [9], whereas the UPQC and the iUPQC and fuzzy logic control have been selected as solution for more specific applications [21]. Moreover, these last ones are used only in particular cases, where their relatively high costs are justified by the power quality improvement it can provide, which would be unfeasible by using conventional solutions. By joining the extra functionality like a STATCOM in the iUPQC device, a wider scenario of applications can be reached, particularly in case of distributed generation in smart grids and as the coupling device in grid-tied micro grids.

In [16], the performance of the iUPQC and the UPQC was compared when working as UPQCs. The main difference between these compensators is the sort of source emulated by the series and shunt power converters. In the UPQC approach, the series converter is controlled as a nonsinusoidal voltage source and the shunt one as a nonsinusoidal current source. Hence, in real time, the UPQC controller and fuzzy logic controller has to determine and synthesize accurately the harmonic voltage and current to be compensated. On the other hand, in the iUPQC approach, the series converter behaves as a controlled sinusoidal current source and the shunt converter as a controlled sinusoidal voltage source. It is not necessary to determine the harmonic voltage and current to be compensated, since the harmonic voltages appear naturally across the series current source and the harmonic currents flow naturally into the shunt voltage source.

As the switching frequency increases, the power rate capability is reduced in actual power converters. Therefore, the iUPQC offers better solutions if compared with the UPQC in case of high-power applications, since the iUPQC compensating references are pure sinusoidal waveforms at the fundamental frequency. Moreover, the UPQC has higher switching losses due to its higher switching frequency. This paper proposes an using fuzzy logic controler, which expands the iUPQC functionalities. This fuzzy logic controler version of iUPQC controller includes all functionalities of those previous ones, including the voltage regulation at the load-side bus, and now providing also voltage regulation at the grid-side bus, like a STATCOM to the grid. Simulation results are provided to validate the new controller design. This paper is organized in five sections. After this introduction, in Section II, the iUPQC applicability is explained, as well as the novel feature of the proposed controller. Section III presents the proposed fuzzy logic controller and an analysis of the power flow in steady state. Finally, Sections IV and V provide the simulation results and the conclusions, respectively.

2.0 EQUIPMENT APPLICABILITY



Figure 1 depicts an electrical system with two buses in spotlight, i.e., bus A and bus B. Bus A is a critical bus of the power system that supplies sensitive loads and serves as point of coupling of a micro grid, in order to clarify the applicability of the improved iUPQC controller. Bus B is a bus of the micro grid, nonlinear loads are connected to the bus B, which requires premium-quality power supply. The voltages at buses A and B must be regulated, in order to properly supply the sensitive loads and the nonlinear loads. The effects caused by the harmonic currents drawn by the nonlinear loads should be mitigated, avoiding harmonic voltage propagation to bus A.

The voltage regulation at bus A is not enough because the harmonic currents drawn by the nonlinear loads are not mitigated by use of a STATCOM. On the other hand, a UPQC or an iUPQC between bus A and bus B can compensate the harmonic currents of the nonlinear loads and compensate the voltage at bus B, in terms of voltage harmonics, unbalance, and sag/swell. Nevertheless, this is still not enough to guarantee the voltage regulation at bus A. Hence, to achieve all the desired goals, a STATCOM at bus A and a UPQC (or an iUPQC) between buses A and B should be employed. However, the costs of this solution would be unreasonably high.

Note that the modified iUPQC serves as an intertie between buses A and B. Moreover, the micro grid connected to the bus B could be a complex system comprising distributed generation, energy management system, and fuzzy logic control systems involving micro grid, as well as smart grid concepts. An attractive solution would be the use of a modified iUPQC controller to provide also reactive power support to bus A, in addition to all those functionalities of this equipment, as presented. In summary, the modified iUPQC can provide the following functionalities:

- a) "smart" circuit breaker as an intertie between the grid and the microgrid;
- b) energy and power flow control between the grid and the microgrid (imposed by a tertiary control layer for the microgrid);
- c) reactive power support at bus A of the power system;
- voltage/frequency support at bus B of the microgrid;
- e) harmonic voltage and current isolation between bus A and bus B (simultaneous grid-voltage and load-current activefiltering capability);

f)

voltage and current imbalance compensation.

Figure 2 depicts, in detail, the connections and measurements of the iUPQC between bus A and bus B. The functionalities (d)–(f) previously listed were extensively explained and verified through simulations analysis [14]–[18], whereas the functionality (c) comprises the original contribution of the present work.

Using fuzzy the series converter of a conventional iUPQC uses only an active-power control variable p, in order to synthesize a fundamental sinusoidal current drawn from bus A, corresponding to the active power demanded by bus B. As a result, the shunt converter has no further degree of freedom in terms of compensating active- or reactive-power variables to expand its functionality. According to the conventional iUPQC controller, the shunt converter imposes a controlled sinusoidal voltage at bus B, which corresponds to the aforementioned functionality (d).

Necessary to provide an energy source (or large energy storage) associated to the dc link of the iUPQC is follow the case. The iUPQC can serve as: a) "smart" circuit breaker and as b) power flow controller between the grid and the microgrid only if the compensating active- and reactive-power references of the series converter can be set arbitrarily. In this case, it is The last degree of freedom is represented by a reactivepower control variable q for the series converter of the iUPQC.





In this way, the iUPQC will provide reactivepower compensation like a STATCOM to the bus A of the grid. As it will be confirmed, this functionality can be added into the fuzzy controller without degrading all other functionalities of the iUPQC.

3.0 IMPROVED IUPQC CONTROLLER

3.1 Main Controller

Figure 3 shows the proposed controller. Figure 2 depicts the iUPQC hardware and the measured units of a three-phase three-wire system that are used in the controller. The controller inputs are the voltages at buses A and B, the current demanded by bus B (iL), and the voltage vDC of the common dc link. By using fuzzy logic controler the outputs are the shunt-voltage reference and the series-current reference to the pulsewidth modulation (PWM) controllers. The voltage and current PWM controllers can be as simple as those employed in [18], or be improved further to better deal with voltage and current imbalance and harmonics [23]–[28].

First, the simplified Clark transformation is applied to the measured variables. As example of this transformation, the grid voltage in the $\alpha\beta$ -reference frame can be calculated as

$$\begin{bmatrix} V_{A_\alpha} \\ V_{A_\beta} \end{bmatrix} = \begin{bmatrix} 1 & 1/2 \\ 0 & \sqrt{3}/2 \end{bmatrix} \begin{bmatrix} V_{A_ab} \\ V_{A_bc} \end{bmatrix} \dots \dots (1)$$

Consequently, the signals sent to the PWM controller are the phase-locked loop (PLL) outputs with amplitude equal to 1 p.u. The shunt converter imposes the voltage at bus B. Thus, it is necessary to synthesize sinusoidal voltages with nominal amplitude and frequency. There are many possible PLL algorithms, which could be used in this case, as verified in [29]–[33].

In the original approach of iUPQC, this current is calculated through the average active power required by the loads PL plus the power PLoss. The series converter synthesizes the current drawn from the grid bus (bus A). The load active power can be estimated by

$$P_{L} = V_{+1_{\alpha}} \cdot i_{L_{\alpha}} + V_{+1_{\beta}} \cdot i_{L_{\beta}} \qquad \dots (2)$$

where iL_{α} , iL_{β} are the load currents, and V+1_ α , V+1_ β are the voltage references for the shunt converter. A low-pass filter is used to obtain the average active power (PL).

The losses in the power converters and the circulating power to provide energy balance inside the iUPQC are calculated indirectly from the measurement of the dc-link voltage. In other words, the power signal PLoss is determined by a fuzzy logic controller (fuzzy block in Figure 3), by comparing the measured dc voltage VDC with its reference value.

The Figure 3 shows additional control loop to provide voltage regulation like a STATCOM at the grid bus is represented by the control signal QSTATCOM. This control signal is obtained through a fuzzy logic controller, in which the input variable is the error between the reference value and the actual aggregate voltage of the grid bus, given by

$$V_{col} = \sqrt{V_{A+1_{\alpha}}^2 + V_{A+1_{\beta}}^2}.$$
...(3)

The sum of the power signals PL and PLoss composes the active-power control variable for the series converter of the iUPQC (p) described in Section II. Likewise, QSTATCOM is the reactive-power control variable q. Thus, the current references $i+1\alpha$ and $i+1\beta$ of the series converter are determined by

$$\begin{bmatrix} i_{+1_\alpha} \\ i_{+1_\beta} \end{bmatrix} = \frac{1}{V_{A+1_\alpha}^2 + V_{A+1_\beta}^2} \begin{bmatrix} V_{A+1_\alpha} & V_{A+1_\beta} \\ V_{A+1_\beta} & V_{A+1_\alpha} \end{bmatrix} \\ \times \begin{bmatrix} \overline{P}_L + \overline{P}_{LOSS} \\ \overline{Q}_{STATCOM} \end{bmatrix}. \qquad \dots (4)$$

3.2. Power Flow in Steady State

The following procedure, based on the average power flow, is useful for estimating the power ratings of the iUPQC converters.



According to Figure 4, the compensation of a voltage sag/swell disturbance at bus B causes a positivesequence voltage at the coupling transformer (Vseries 0), since VA VB. For combined series-shunt power conditioners, such as the UPQC and the iUPQC, only the voltage sag/swell disturbance and the power factor (PF) compensation of the load produce a circulating average power through the power conditioners [34], [35]. Moreover, Vseries and iPB in the coupling transformer leads to a circulating active power Pinner in the iUPQC. Additionally, the compensation of the load PF increases the current supplied by the shunt converter. The following analysis is valid for an iUPQC acting like a conventional UPQC or including the extra compensation like a STATCOM.

For simplicity, the losses in the iUPQC will be neglected to these follows. First, the circulating power will be calculated when the iUPQC is operating just like a conventional UPQC. Afterward, the equations will include the STATCOM functionality to the grid bus A. In both cases, it will be assumed that the iUPQC controller is able to force the shunt converter of the iUPQC to generate fundamental voltage always in phase with the grid voltage at bus A.

For the first case, the following average powers in steady state can be determined:

$$\bar{S}_A = \bar{P}_B \qquad \dots (5)$$

$$\bar{Q}_{shunt} = -\bar{Q}_B \qquad \dots (6)$$

$$\overline{Q}_{series} = \overline{Q}_A = 0 \ var \qquad \dots (7)$$

$$\overline{P}_{series} = \overline{P}_{shunt} \qquad \dots (8)$$

where SA and QA are the apparent and reactive power injected in the bus A; PB and QB are the active and reactive power injected in the bus B; Pshunt and Qshunt are the active and reactive power drained by the shunt converter; Pseries and Qseries are the active and reactive power supplied by the series converter, respectively.

The constraint of keeping unitary the PF at bus A derived to equations (5) and (8). In this case, the current passing through the series converter is responsible only for supplying the load active power, that is, it is in phase (or counterphase) with the voltages VA and VB. Thus, (7) can be stated. Consequently, the coherence of the power flow is ensured through (8).

If a voltage sag or swell occurs, Pseries and Pshunt will not be zero, and thus, an inner-loop current (iinner) will appear. The series and shunt converters and the aforementioned circulating active power (Pinner) flow inside the equipment. It is convenient to define the following sag/swell factor. Considering VN as the nominal voltage

$$k_{sag/swell} = \frac{\left|\dot{V}_{A}\right|}{\left|\dot{V}_{N}\right|} = \frac{V_{A}}{V_{N}} \qquad \dots (9)$$

From (5) and considering that the voltage at bus B is kept regulated, i.e., VB = VN, it follows that

$$\sqrt{3}. k_{sag/swell}. V_N. i_S = \sqrt{3}. V_N. i_{P_B}$$
$$i_S = \frac{i_{P_B}}{k_{sag/swell}} = i_{\overline{P}_B} + i_{inner} \qquad \dots (10)$$

$$i_{inner} = \left| i_{P_B} \left(\frac{1}{K_{sag/swell} - 1} \right) \right|. \tag{11}$$

$$P_{inner} = P_{series} = P_{shunt} = 3(V_B - V_A)(i_{P_B} + i_{inner}). \qquad ...(12)$$

The circulating power is given by

$$\overline{P}_{inner} = 3(V_N - V_A) \left(\frac{\overline{P}_B}{3V_N} \frac{1}{k_{sag/swell}} \right) \qquad \dots (13)$$

From (11) and (12), it follows that

$$\overline{P}_{inner} = \overline{P}_{sreies} = \overline{P}_{shunt} = \frac{1 - K_{sag/swell}}{k_{sag/swell}} \overline{P}_{B}.$$
 ...(14)

In order to verify the effect on the power rate of the series and shunt converters, a full load system SB = P 2 B + Q 2 B = 1p.u. with PF ranging from 0 to 1 was considered. Thus, (14) demonstrates that Pinner depends on the active power of the load and the sag/swell voltage disturbance. It was also considered the sag/swell voltage disturbance at bus A ranging ksag/swell from 0.5 to 1.5. In this way, the power rating of the series and shunt converters are obtained through (6)–(8) and (14).

The apparent power of the series and shunt power converters depicts to the Figure 5. In these figures, the ksag/swell-axis and the PF-axis are used to evaluate the power flow in the series and shunt power converters according to the sag/ swell voltage disturbance and the load power consumption, respectively. The power flow in the series converter indicates that a high power is required in case of sag voltage disturbance with high active power load consumption.



If the iUPQC performs all original UPQC functionalities together with the STATCOM functionality, the voltage at bus A is also regulated with the same phase and magnitude, that is, 'V A = 'VB = 'VN, and then, the positive sequence of the voltage at the coupling transformer is zero (V Series = 0). Thus, in steady state, the power flow is determined by

$\bar{S}_A = \bar{P}_B + \bar{Q}_{STATCOM}$	(15)
$\overline{Q}_{STATCOM} + \overline{Q}_{SERIES} = \overline{Q}_{SHUNT} + \overline{Q}_B$	(16)

$$\bar{Q}_{series} = 0 \ var \qquad \dots (17)$$

$$\overline{P}_{series} = \overline{P}_{inner} = 0 W \qquad \dots (18)$$

Ideally, the STATCOM functionality mitigates the inner-loop active power flow (Pinner), and the power flow in the series converter is zero. Where QSTATCOM is the reactive power that provides voltage regulation at bus A. Consequently, by using fuzzy control if the series converter is properly designed along with the coupling transformer to synthesize the controlled currents $I+1_{\alpha}$ and $I+1_{\beta}$, as shown in Figure 3, then a lower power converter can be employed. Contrarily, the shunt converter still has to provide the full reactive power of the load and also to drain the reactive power injected by the series converter to regulate the voltage at bus A.

4.0 FUZZY LOGIC CONTROLLER

FLC is one of the most successful operations of fuzzy set theory. Its chief aspects are the exploitation of linguistic variables rather than numerical variables. FL control technique relies on human potential to figure out the system behavior and is constructed on quality control rules. The basic structure of an FLC is represented in Figure6.



- A Fuzzification interface alters input data into suitable linguistic values.
- A Knowledge Base which comprises of a data base along with the essential linguistic definitions and control rule set.

• A Decision Making Logic which collects the fuzzy control action from the information of the control rules and the linguistic variable descriptions

TABLE 1 FUZZY RULE REPRESENTATION									
erroe	NB	NM	NS	Ζ	PS	PM	PB		
NB	PB	PB	PB	PM	PM	PS	Ζ		
NM	PB	PB	PM	PM	PS	Ζ	Ζ		
NS	PB	PM	PS	PS	Ζ	NM	NB		
Ζ	PB	PM	PS	Ζ	NS	NM	NB		
PS	PM	PS	Ζ	NS	NM	NB	NB		
PM	PS	Ζ	NS	NM	NM	NB	NB		
PB	Ζ	NS	NM	NM	NB	NB	NB		



 A Defuzzification interface which surrenders a non fuzzy control action from an inferred fuzzy control action. In this paper, an advanced control strategy, FLC is implemented along with UPQC for voltage correction through Series APF and for current regulation through Shunt APF. Error and Change in Error are the inputs and Duty cycle is the output to the Fuzzy Logic Controller as shown in Figure 7-Figure9

In the decision-making process, there is rule base that links between input (error signal) and output signal. Table II shows the rule base exercised in this proposed Fuzzy Logic Controller.

5.0 SIMULATION RESULTS

The controller was embedded in a fixed-point digital signal processor (TMS320F2812).The improved iUPQC controller, as shown in Figure 3, was verified in a 5-kVA prototype, whose parameters are presented in Table I.

TABLE 2				
IUPQC PROTOTYPE PARAMETERS				
Parameter	Values			
Voltage	220 V _{rms}			
Grid frequency	50 Hz			
Power rate	5 KVA			
DC-link Voltage	450 V _{dc}			
DC- link capacitor	C= 9400µF			
Shunt converter passive filter	L=750 µH			
	R=3.7 Ω			
	C=20.0 µF			
Series converter passive filter	L=1.0 mH			
	R=7.5 Ω			
	C=20.0 µF			
Sampling frequency	19440 Hz			
Switching Frequency	9720 Hz			
PI controller (P _{Loss})	$K_{p} = 4.0$			
	$K_i = 250.0$			
PI Controller(Q _{STATCOM})	$K_p = 0.5$			
	$K_i = 50.0$			

In this paper in order to verify all the power quality issues described, the iUPQC was connected to a grid with a voltage sag system, as depicted in Figure 6. The voltage sag system was composed by an inductor (LS), a resistor (RrmSag), and a breaker (SSag). To cause a voltage sag at bus A, SSag is closed. At first, the source voltage regulation was tested with no load connected to bus B. In this case, the iUPQC behaves as a STATCOM, and the breaker SSag is closed to cause the voltage sag.





In this simulation case, LS = 10 mH, and RSag = 7.5 Ω To verify the grid-voltage regulation (see Figure 7), the control of the QSTATCOM variable is enabled to compose (4) at instant t = 0 s.. As shown in Figure 7 before the QSTATCOM variable is enabled, only the dc link and the voltage at bus B are regulated, and there is a voltage

sag at bus A. After t = 0s, the iUPQC starts to draw reactive current from bus A, increasing the voltage until its reference value. As shown in Figure 7, the load voltage at bus B is maintained regulated during all the time, and the grid-voltage regulation of bus A has a fast response.



6.0 CONCLUSION

By using fuzzy logic control to improved iUPQC controller, the currents synthesized by the series converter are determined by the average active power of the load and the active power to provide the dc-link voltage regulation, together with an average reactive power to regulate the grid-bus voltage. In addition to all the power-quality compensation features and fuzzy logic of a conventional UPQC or an iUPQC, this improved controller also mimics a STATCOM to the grid bus. This new feature enhances the applicability of the iUPQC and provides new solutions in future scenarios involving smart grids and micro grids, including distributed generation and energy storage systems to better deal with the inherent variability of renewable resources such as solar and wind power.Despite the addition of one more power-quality compensation feature, the grid-voltage regulation reduces the inner-loop circulating power inside the iUPQC, which would allow lower power rating for the series converter. Moreover, the improved iUPQC controller may justify the costs and promotes the iUPQC applicability in power quality issues of critical systems, where it is necessary not only an iUPQC or a STATCOM, but both, simultaneously. These results have demonstrated a suitable performance of voltage regulation at both sides of the iUPQC,

even while compensating harmonic current and voltage imbalances. The simulation results verified the improved iUPQC goals. The gridvoltage regulation was achieved with no load, as well as when supplying a three-phase nonlinear load.

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